

ABSTRACT:

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An integrated circuit is provided with at least two output drivers (4) without substrate contacts. The integrated circuit is further provided with at least a core with a Vssc contact (7, 9) and a periphery provided with at least one Vssq contact (8). A resistance (11) with a value of between 100 and 300 ohms lies between each Vssq contact (8) and the Vssc contact (7, 9). The value of the resistance (11) is preferably greater than 250 ohms in the case of output drivers which are not slew-rate controlled, and the value of the resistance (11) is preferably at most 250 ohms in the case of slew-rate controlled output drivers. The resistance (11) may be provided in the Vssq pad.

Fig. 1